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Substitute for form 1449/PTO SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known		
				Application Number	10/695,498	
Sheet		1	of	1	Filing Date	October 29, 2003
				First Named Inventor	Abbas Amirichimeh	
				Art Unit	2609- 2616	
				Examiner Name	Alia, Curtis A. R. Jain	
				Attorney Docket Number	1875.3640003/JTH/TAD	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)			
	US1				
	US2				
	US3				
	US4				
	US5				
	US6				
	US7				
RA	US8	4,627,070	12-02-1986	Champlin et al.	
RA	US9	6,137,734	10-24-2000	Schoner et al.	
	US10				
	US11				
	US12				
	US13				
	US14				
	US15				
	US16				
	US17				
	US18				
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (If known)				
	FP1					
	FP2					
	FP3					
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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Examiner Signature	R. Jain	Date Considered	8/7/07
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
RA	NPL1	International Search Report issued December 14, 2004, for International Application No. PCT/US003/34234, 5 pages.	
	NPL2	Alaska Quad-Port Gigabit Ethernet Over Copper Transceivers, from http://www.marvell.com/products/transceivers/quadport/... , 1 page, printed May 8, 2007.	
	NPL3	Alaska X10 Gigabit Ethernet Alaska Gigabit Ethernet Fast Ethernet Physical Layer (PHY) Transceiver Families Provide a Full Range of Ethernet Transceiver Solutions for the Broadband Communications Industry, from http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/phy_transceiver.asp , 4 pages, Copyright 2007, printed May 8, 2007.	
	NPL4	Cisco Search: Results for "cdl" within "News@Cisco", from http://www.cisco.com/pcgi-bin/search/search.pl , Copyright 1992-2003, printed July 29, 2003.	
	NPL5	Industry Breakthrough: Marvell Announces the First Quad-Port Transceiver to Support Both Copper and Fiber-Optic Gigabit Ethernet Interfaces, from Business Wire at www.encyclopedia.com/printable.aspx?id=1G1:68912211 , 4 pages, January 9, 2001, printed May 8, 2007.	
	NPL6	David Maliniak (ed.), Bel's integrated connector modules support Marvell's Alaska quad Gigabit Ethernet transceiver, from http://www.electronicweb.com/Content/news/... , 1 page, December 14, 2000, printed May 8, 2007.	
	NPL7	Marvell Gets Small, from http://www.lightreading.com/document.asp?doc_id=12004&print=true , 1 page, February 19, 2002, printed May 8, 2007.	
	NPL8	Marvell Introduces the Industry's Smallest Quad-Port Gigabit Transceiver Device, Enabling Ultra High Port Density Enterprise Switching Systems, from http://www.marvell.com/press/pressNewsDisplay.do?releaseID=41 , 3 pages, February 19, 2002, printed May 8, 2007.	
	NPL9	Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, from http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html , 3 pages, Copyright 1992-2003, printed July 29, 2003.	
	NPL10	Ed Turner and David Law, IEEE P802.3ae MDC/MDIO, from http://www.ieee802.org/3/efm/public/sep01/turner_1_0901.pdf , 21 pages, Sept. 17-19, 2001.	

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Examiner Signature	<i>R. Jain</i>	Date Considered	8/7/07
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